

IN THE CLAIMS

Please amend claims 1, 10 , and 19 as indicated below.

1. (Currently Amended) A memory card interface apparatus comprising:
a plurality of memory card interfaces, with at least a subset of the plurality of memory card interfaces ~~configured~~ being selectively operable to interface with a plurality of memory cards of a first type, ~~the plurality of memory card interfaces accessible~~ in parallel.
2. (Original) The apparatus of claim 1, wherein at least one of the memory card interfaces is configured to read a plurality of different memory card types.
3. (Original) The apparatus of claim 1, wherein at least one of the memory card interfaces includes an indicator identifying a status of an operation for a respective memory reader interface.
4. (Original) The apparatus of claim 1, wherein the indicator includes a light indicating data is being written to a card in the respective memory card interface.
5. (Original) The apparatus of claim 1, wherein at least one of the memory card interfaces is configured to interface with a Write Once Read Many (WORM) memory card.
6. (Original) The apparatus of claim 1, wherein mechanical pins, of at least one of the plurality of memory card interfaces, are inserted directly into a backbone of the apparatus.

7. (Original) The apparatus of claim 1, wherein a first subset of the plurality of memory card interfaces are configured to interface with a memory card of a first type and a second subset of the plurality of memory card interfaces are configured to interface with a memory card of a second type, wherein the first and second subset of memory card interfaces are accessible in parallel.

8. (Original) The apparatus of claim 1, wherein the apparatus includes a text display, wherein text on the display is manipulated using Simple Display Device commands.

9. (Original) The apparatus of claim 1, wherein the apparatus includes a text display, wherein text on the display is manipulated using Small Computer System Interface commands.

10. (Currently Amended) A system comprising: a controller circuit;
a bus coupled to the controller circuit;

a plurality of memory card interfaces, with at least a subset of the plurality of memory card interfaces ~~configured being~~ selectively operable to interface with a plurality of memory cards of a first type, ~~the plurality of memory card interfaces accessible~~ in parallel.

11. (Original) The system of claim 8, wherein at least one of the memory card interfaces is configured to interface with a plurality of different memory card types.

12. (Original) The system of claim 8, wherein at least one of the memory card interfaces includes an indicator identifying a status of an operation for a respective memory card interface.

13. (Original) The system of claim 10, wherein the indicator includes a light indicating data is being written to a card in the respective memory card interface.

14. (Original) The system of claim 8, wherein at least one of the memory card interfaces is configured to interface with a Write Once Read Many (WORM) memory card.

15. (Original) The system of claim 8, wherein mechanical pins, of at least one of the plurality of memory card interfaces, are inserted directly into a backbone of the system.

16. (Original) The system of claim 8, wherein a first subset of the plurality of memory card interfaces are configured to interface with a memory card of a first type and a second subset of the plurality of memory card interfaces are configured to interface with a memory card of a second type, wherein the first and second subset of memory card reader interfaces are accessible in parallel.

17. (Original) The system of claim 10, wherein the system includes a text display, wherein text on the display is manipulated using Simple Display Device commands.

18. (Original) The system of claim 10, wherein the system includes a text display, wherein text on the display is manipulated using Small Computer System Interface commands.

19. (Currently Amended) A method comprising:

providing access to a plurality of memory card interfaces, with at least a subset of the plurality of memory card interfaces configured to interface with a memory card of a first type;

and

selectively operating the subset of memory cards to ~~providing parallel~~ access to the plurality of memory card interfaces in parallel.

20. (Original) The method of claim 19, wherein at least one of the memory card interfaces is configured to interface with a plurality of different memory card types.

21. (Original) The method of claim 19, wherein at least one of the memory card interfaces includes an indicator identifying a status of an operation for a respective memory card interface.